

## T0 Package Test Socket, GD18 series Size 18x32.9mm / 0.71x1.30"

### Specifications

Contact Resistance : 50mΩ max  
 Current Rating : 10A at 25°C (Between Drain and Source)\*  
 Room temperature with 30°C T-Rise  
 Withstanding Voltage : DC500V 1min at 25°C  
 Insulation Resistance : 500MΩmin at DC500V  
 Operating Temperature : -40°C~+200°C \*\*

T0-252/D-PAK  
 Burn-in Test  
 Kelvin Contact (Option)

Current Rating 10A

High Temperature 200°C

\* Please contact us for the high current type.  
 \*\* Including terminal temperature rise.

### T0-252/D-PAK Package Test Socket

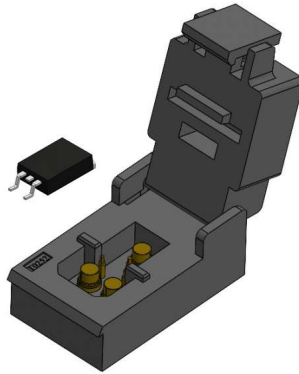
#### How to Order

**GD18 - T0252 - K - 755**

T0-252  
 Package

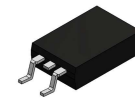
Code

**N** : No Kelvin Contact  
**K** : With Kelvin Contact



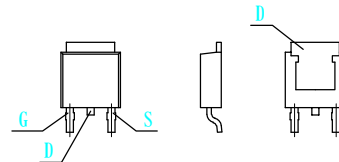
#### Acceptable Device

T0-252/D-PAK



**D** : Drain  
**G** : Gate  
**S** : Source

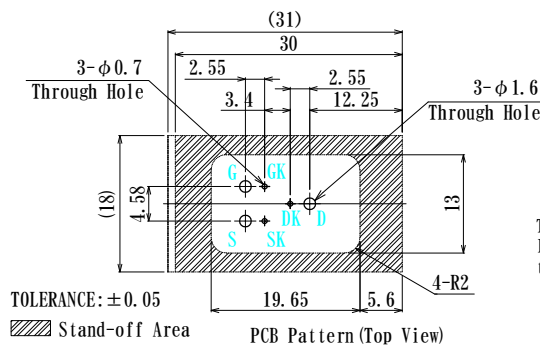
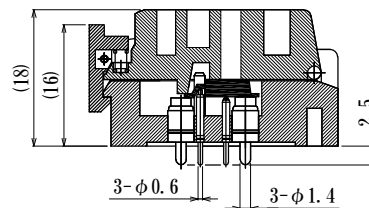
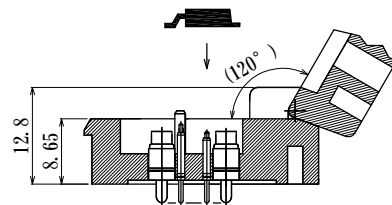
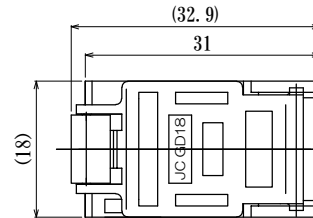
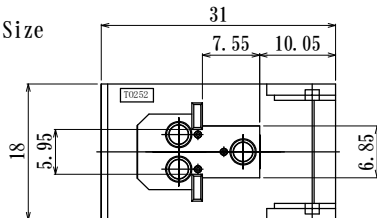
#### Terminal Pattern \*\*\*



\*\*\* Also possible to support other terminal patterns.  
 Please contact us for more details.

P/N **GD18-T0252-K-755**

Actual Size



Thru holes GK, DK, SK are for Kelvin contact.  
 If Kelvin Contact is not required,  
 these holes are not necessary.

TOLERANCE: ±0.05

Stand-off Area

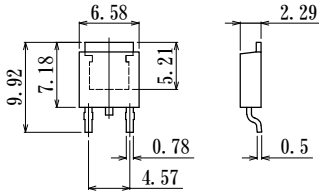
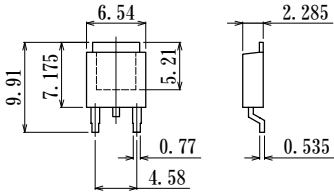
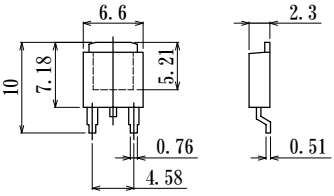
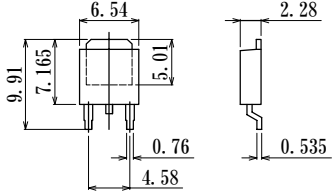
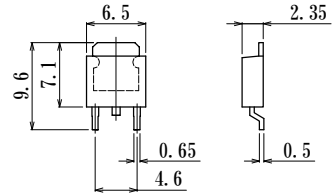
PCB Pattern (Top View)

mm

P/N **GD18-T0252-K-755**  
**GD18-T0252-N-755**

●Acceptable Device Size (Example)

Able to support other device size.  
Please contact us for more information.

 <p>9.92 7.18 6.58 5.21 0.78 4.57 2.29 0.5</p>	 <p>9.91 7.175 6.54 5.21 0.77 4.58 2.285 0.535</p> <p>IRF40R207 (Infineon)</p>	 <p>10 7.18 6.6 5.21 0.76 4.58 2.3 0.51</p> <p>TK560P65Y (TOSHIBA)</p>
 <p>9.91 7.165 6.54 5.01 0.76 4.58 2.28 0.535</p> <p>MJD47 (ON Semiconductor)</p>	 <p>9.6 7.1 6.5 0.65 4.6 2.35 0.5</p> <p>2SD1918 (ROHM)</p>	

Although the device is listed, but it may not be compatible due to the large tolerance of the device dimension.  
Therefore, please send the sample of the device in advance and we will check the compatibility.

## T0 Package Test Socket, GD18 series Size 18x32.9mm / 0.71x1.30"

### Specifications

Contact Resistance : 50mΩ max  
 Current Rating : 10A at 25°C (Between Drain and Source) \*  
 Room temperature with 30°C T-Rise  
 Withstanding Voltage : DC2000V 1min at 25°C  
 Insulation Resistance : 500MΩmin at DC500V  
 Operating Temperature : -40°C~+200°C \*\*

T0-263/D2-PAK  
 Burn-in Test  
 Kelvin Contact (Option)

Current Rating 10A

High Temperature 200°C

\* Please contact us for the high current type.  
 \*\* Including terminal temperature rise.

### T0-263/D2-PAK Package Test Socket

#### How to Order

**GD18 - T0263 - K - 109**

T0-263  
 Package

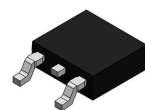
Code

**N** : No Kelvin Contact  
**K** : With Kelvin Contact



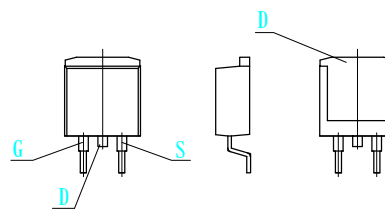
#### Acceptable Device

T0-263/D2-PAK



**D** : Drain  
**G** : Gate  
**S** : Source

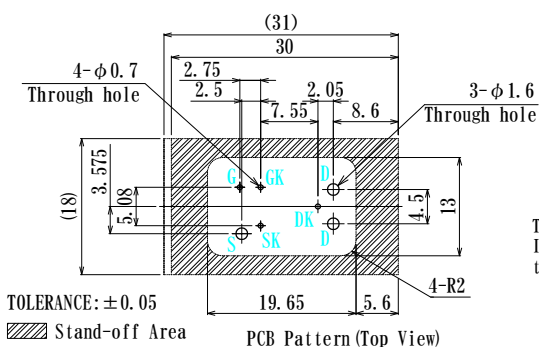
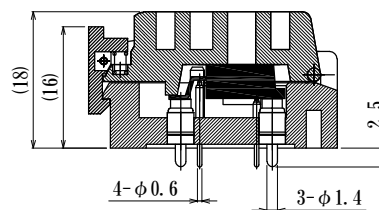
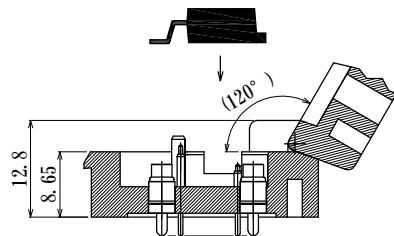
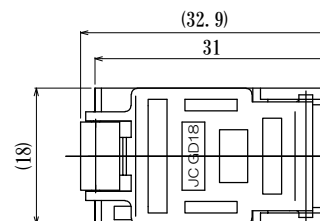
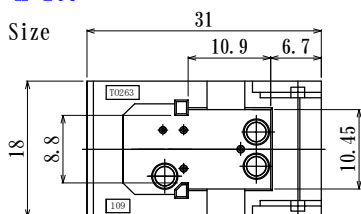
#### Terminal Pattern \*\*\*



\*\*\* Also possible to support other terminal patterns.  
 Please contact us for more details.

P/N **GD18-T0263-K-109**

Actual Size



Thru holes GK, DK, SK are for Kelvin contact.  
 If Kelvin Contact is not required,  
 these holes are not necessary.

mm

P/N **GD18-T0263-K-109**  
**GD18-T0263-N-109**

●Acceptable Device Size (Example)

Able to support other device size.  
Please contact us for more information.

<p>10 8 15.3 10.4 8.4 4.5 2.54 0.8 5.08 0.5</p> <p>FDB28N30 (ON Semiconductor)</p>	<p>10.16 6.22 15.25 10.7 6.86 4.45 2.285 0.75 5.08 0.56</p> <p>IRFS7437PbF (Infineon)</p>	<p>10.2 8.5 15.4 10.5 7.5 4.2 2.54 0.81 5.08 0.52</p> <p>STB11NK50ZT4 (STMicroelectronics)</p>
<p>10.16 7.98 15.24 10.42 7.36 4.45 2.54 0.83 5.08 0.49</p> <p>C3D06060G (CREE)</p>	<p>9.9 15.3 10.4 5.8 4.5 0.8 5.08 0.5</p> <p>SK107114 (Sanken)</p>	<p>10.16 7.23 15.24 10.52 6.75 4.45 0.75 5.08 0.54</p> <p>DMTH4004SCTB (Diodes Incorporated)</p>
<p>10.03 7.62 15.24 10.28 7.96 4.5 2.54 0.83 5.08 0.49</p> <p>IXTA1R6N50D2 (IXYS)</p>		

Although the device is listed, but it may not be compatible due to the large tolerance of the device dimension. Therefore, please send the sample of the device in advance and we will check the compatibility.

P/N **GD18-T0263-K-103**  
**GD18-T0263-N-103**

●Acceptable Device Size (Example)

Able to support other device size.  
Please contact us for more information.

<p>10.16 5.08 14.35 9.91 6.35 4.57 1.98 0.71 5.08</p>	<p>10.2 7.8 15 10 7.16 4.44 2.45 0.76 5.08</p>	
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Although the device is listed, but it may not be compatible due to the large tolerance of the device dimension. Therefore, please send the sample of the device in advance and we will check the compatibility.

## T0 Package Test Socket, GD18 series Size 18x32.9mm / 0.71x1.30"

T0-263-7/D2-PAK  
Burn-in Test  
Kelvin Contact (Option)

Current Rating 12A

High Temperature 200°C

### Specifications

Contact Resistance : 50mΩ max  
Current Rating : 12A at 25°C (Between Drain and Source) \*  
Room temperature with 30°C T-Rise  
Withstanding Voltage : DC2000V 1min at 25°C (Between Drain and Source)  
Insulation Resistance : 500MΩmin at DC500V  
Operating Temperature : -40°C~+200°C \*\*

\* Please contact us for the high current type.  
\*\* Including terminal temperature rise.

### T0-263-7/D2-PAK Package Test Socket

#### How to Order

**GD18 - T0263-7 - K - 109**

T0-263-7  
Package

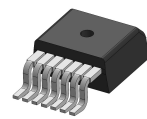
Code

**N** : No Kelvin Contact  
**K** : With Kelvin Contact



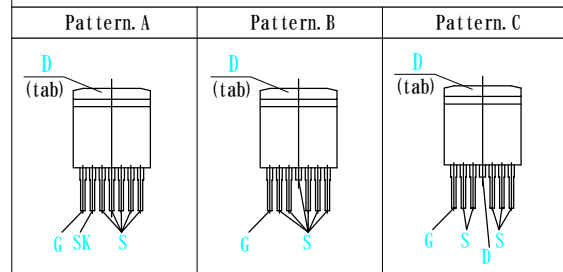
#### Acceptable Device

T0-263-7/D2-PAK



**D** : Drain  
**G** : Gate  
**S** : Source  
**SK** : Kelvin Source

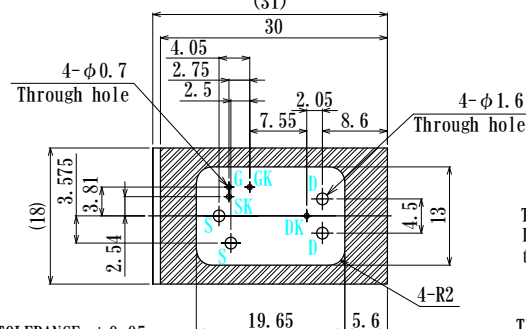
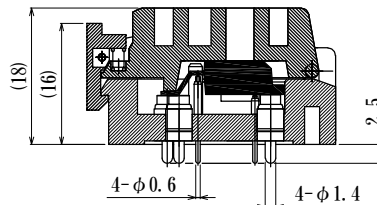
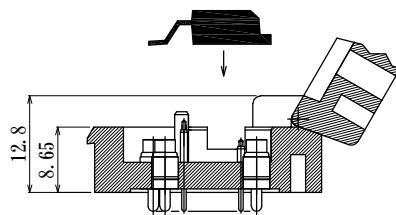
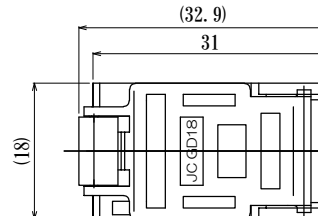
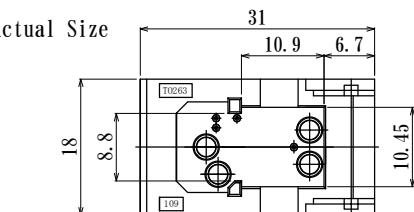
#### Terminal Pattern (Top View) \*\*\*



\*\*\* Also possible to support other terminal patterns.  
Please contact us for more details.

P/N **GD18-T0263-7-K-109**

Actual Size



TOLERANCE: ±0.05

▨ Stand-off Area

PCB Pattern (Top View)

Thru holes GK, DK, SK are for Kelvin contact.  
If Kelvin Contact is not required,  
these holes are not necessary.

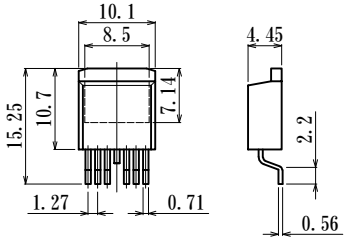
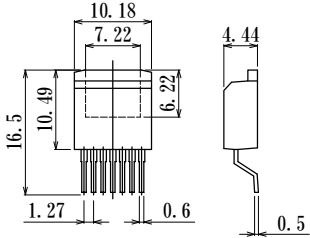
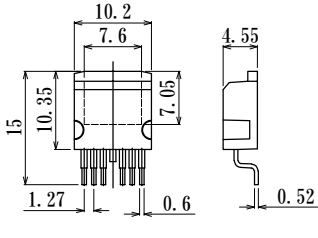
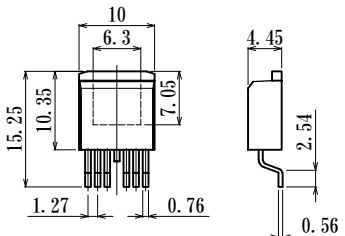
The PCB Pattern is common for Terminal Patterns A, B, and C.

mm

P/N **GD18-T0263-7-K-109**  
**GD18-T0263-7-N-109**

●Acceptable Device Size (Example)

Able to support other device size.  
Please contact us for more information.

 <p>AUIRFS8409-7P (Infineon)</p>	 <p>C3M0075120J (CREE)</p>	 <p>STH300NH02L-6 (STMicroelectronics)</p>
 <p>SQM40016EM (Vishay)</p>		

Although the device is listed, but it may not be compatible due to the large tolerance of the device dimension. Therefore, please send the sample of the device in advance and we will check the compatibility.

GD18 Test Socket Blank Product  
(Product without processing holes and shapes)

Customize products not listed in this catalog could be processed by machining.  
Please let us know the specifications of the device.

P/N **GD18-□□□□□□**

P/N will be informed after fixing specifications.

Actual Size

